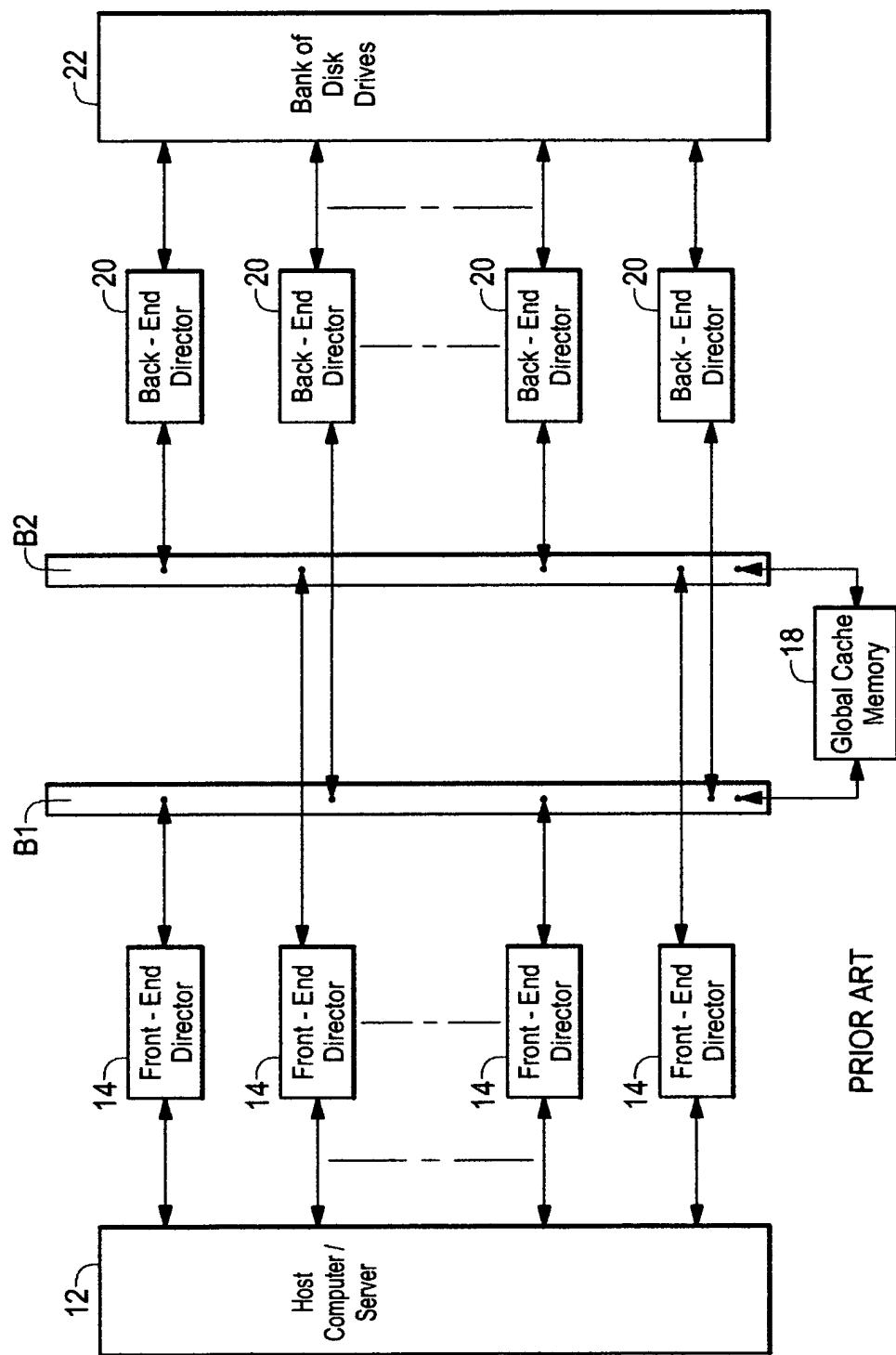




Serial No.: 09/606,730

1/25



PRIOR ART

FIG. 1



Serial No.: 09/606,730

2/25

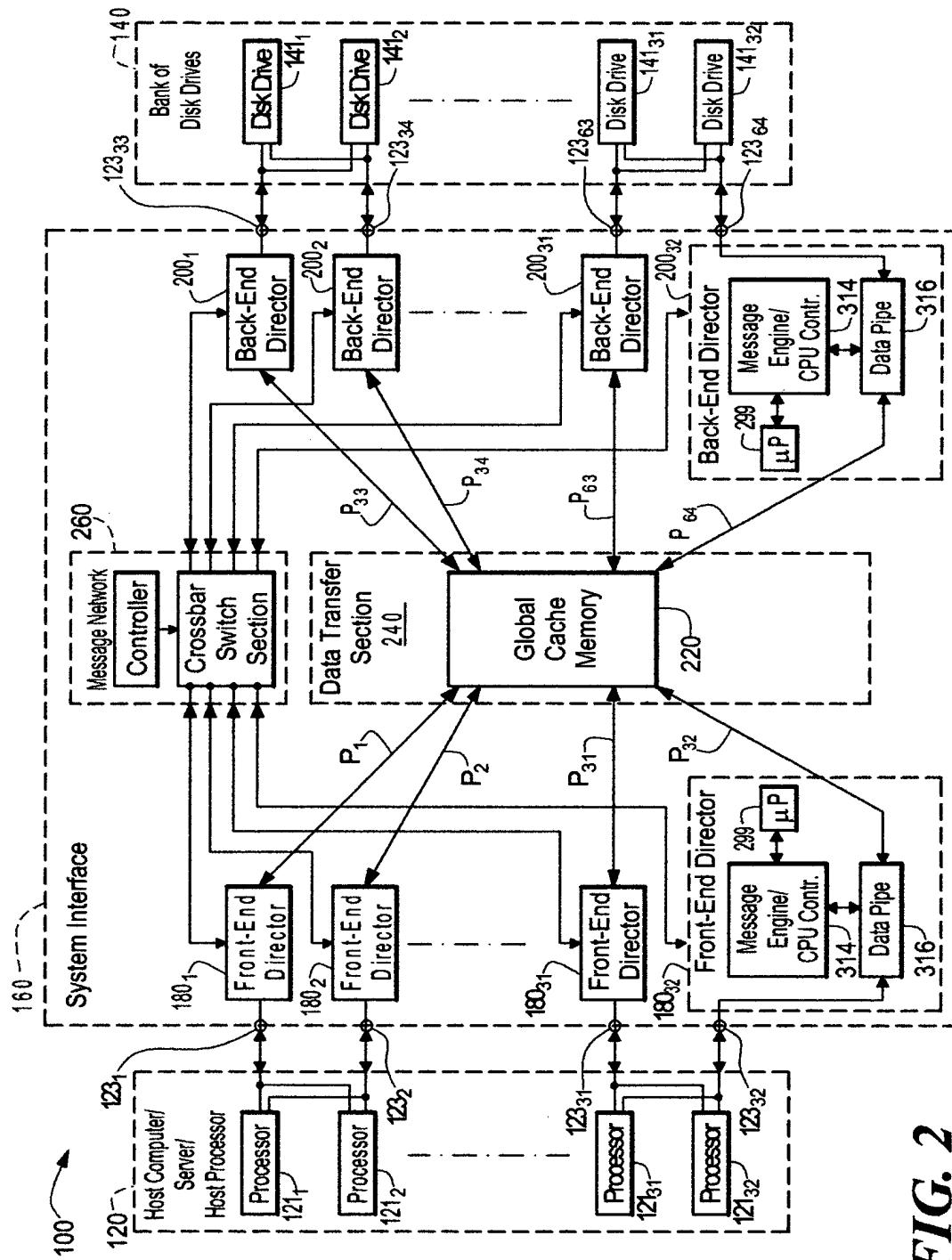
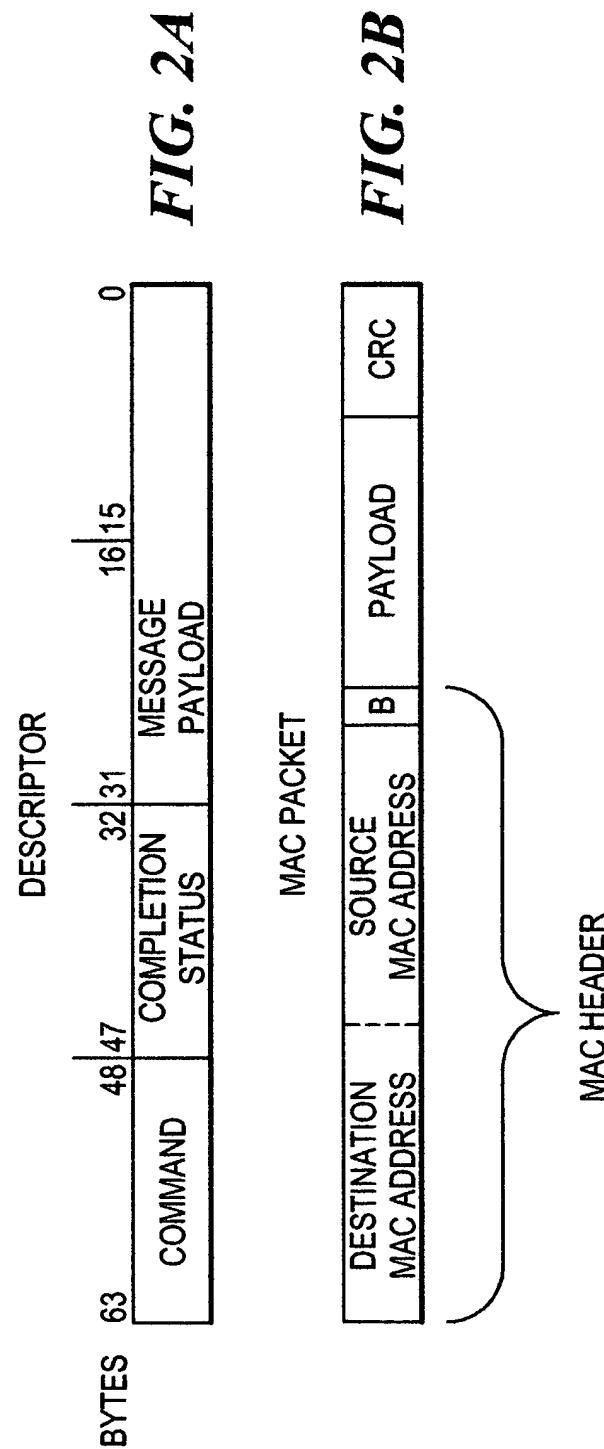


FIG. 2



Serial No.: 09/606,730

3/25





Serial No.: 09/606,730

4/25

FIG. 3

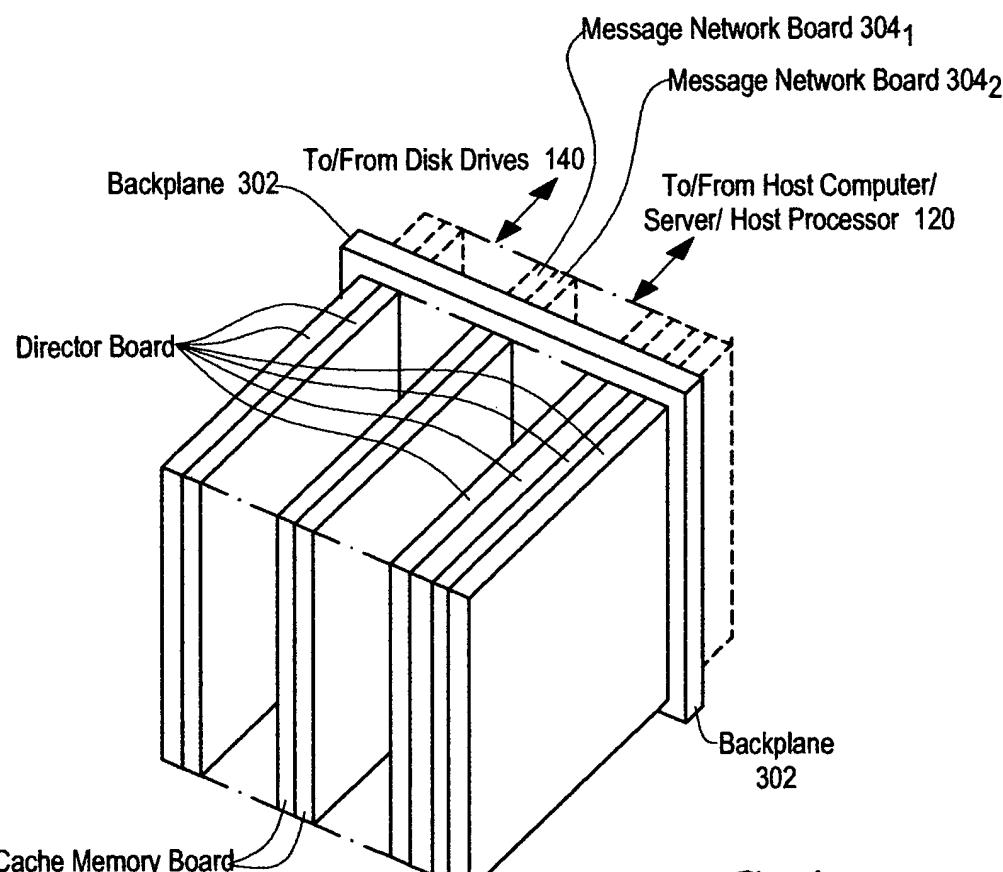
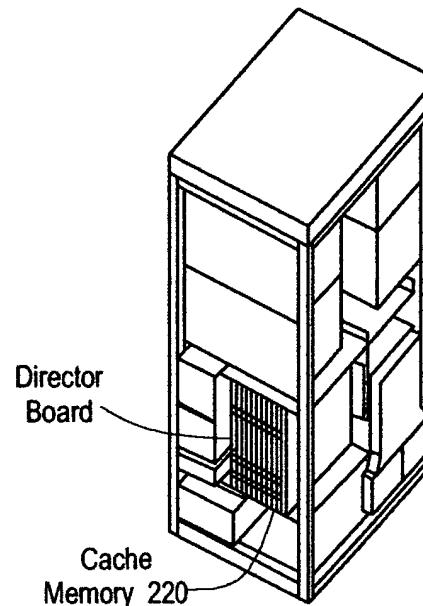


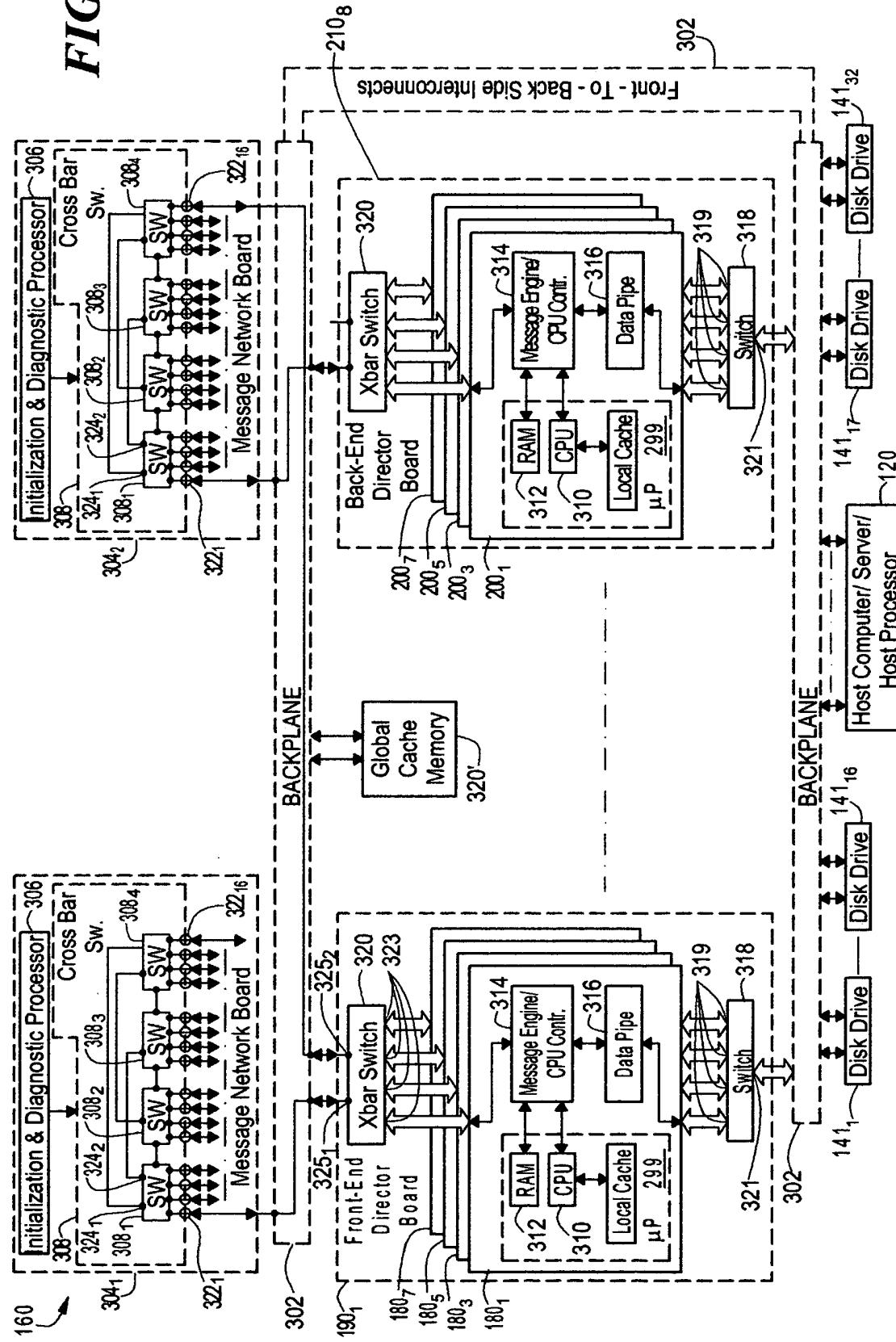
FIG. 4



Serial No.: 09/606,730

5/25

FIG. 5





Serial No.: 09/606,730

6/25

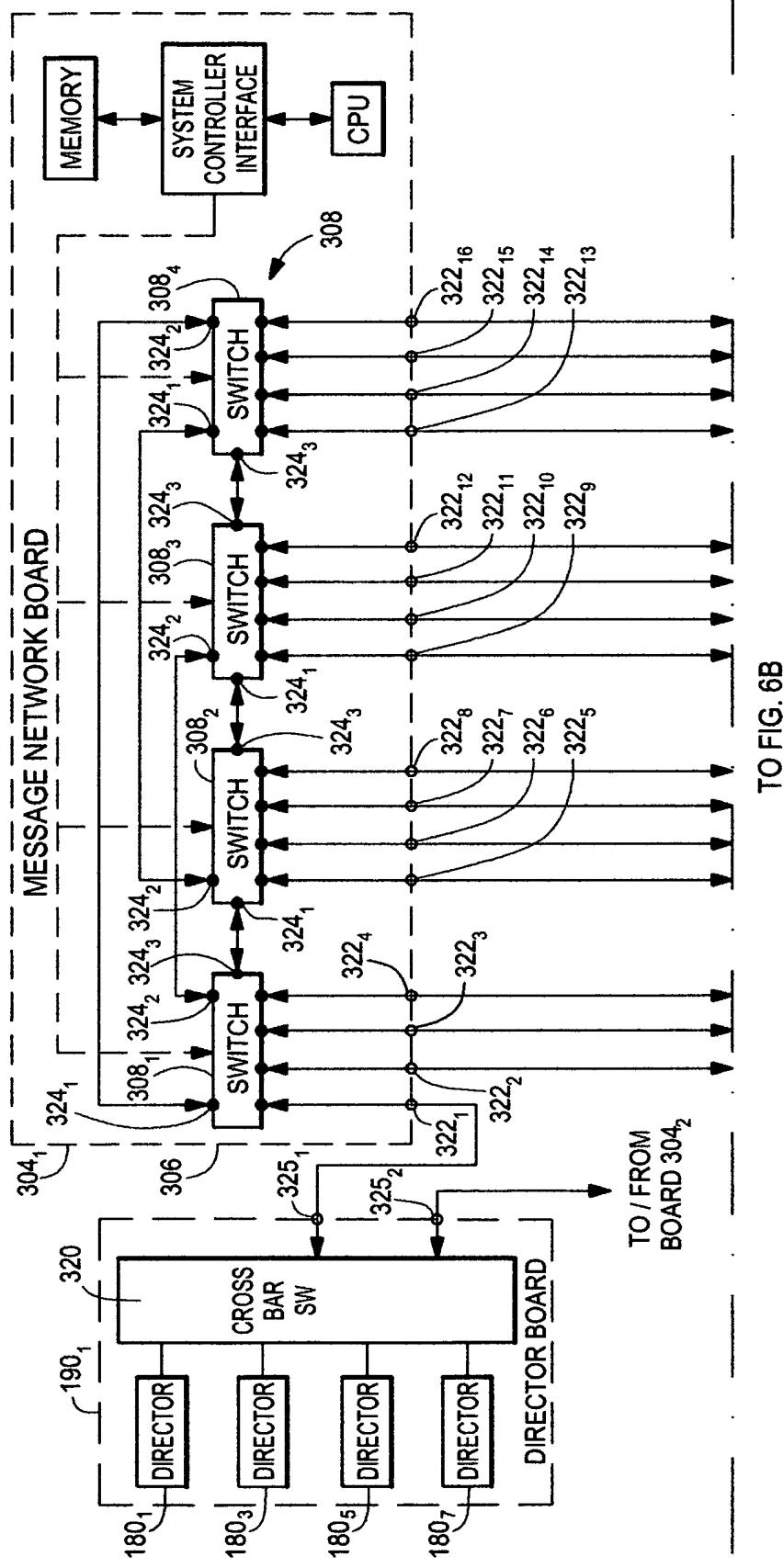


FIG. 6A

FIG. 6

FIG. 6A
FIG. 6B

TO FIG. 6B

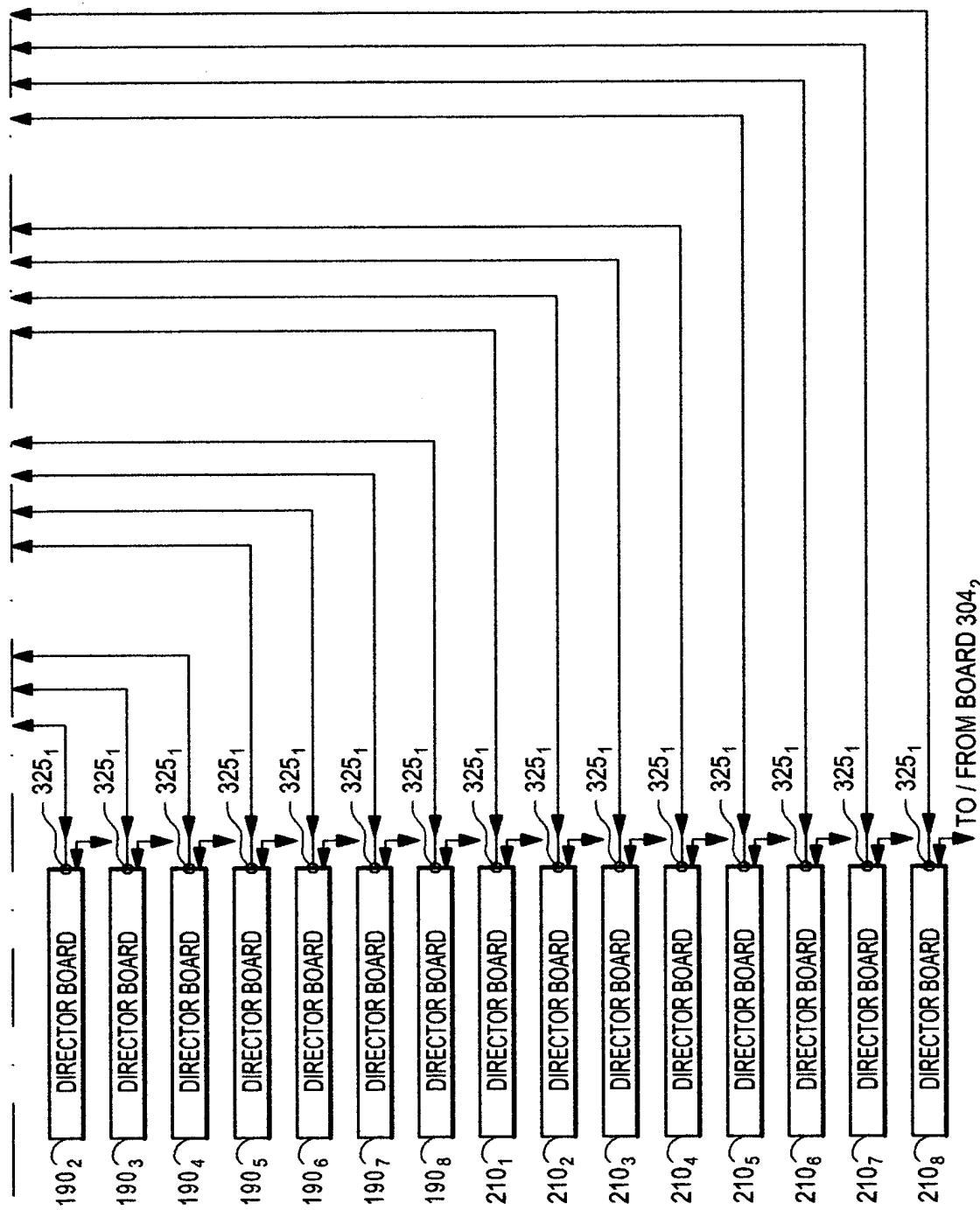


Serial No.: 09/606,730

7/25

FIG. 6B

FROM FIG. 6A





Serial No.:09/606,730

8/25

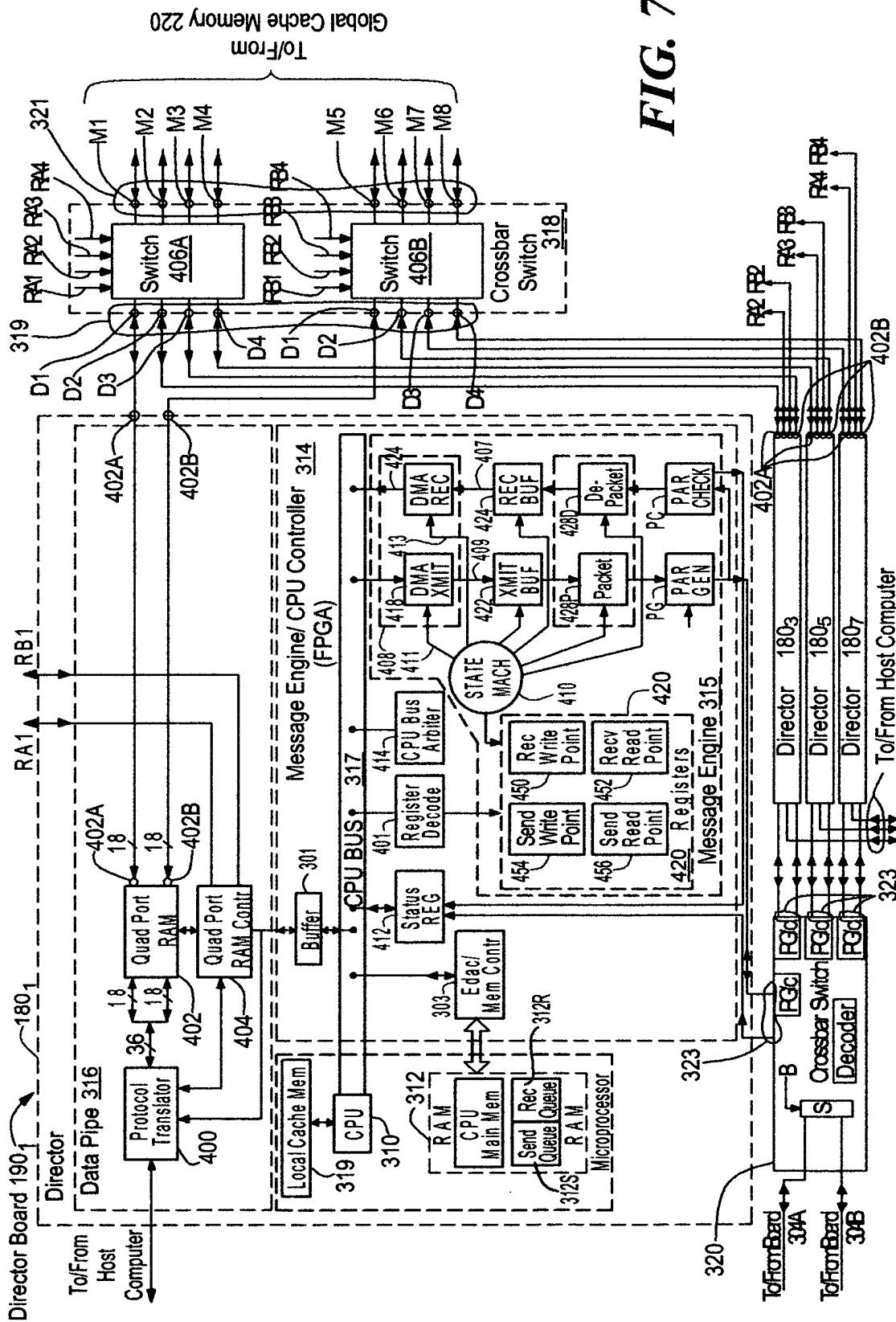


FIG. 7



Serial No.: 09/606,730

9/25

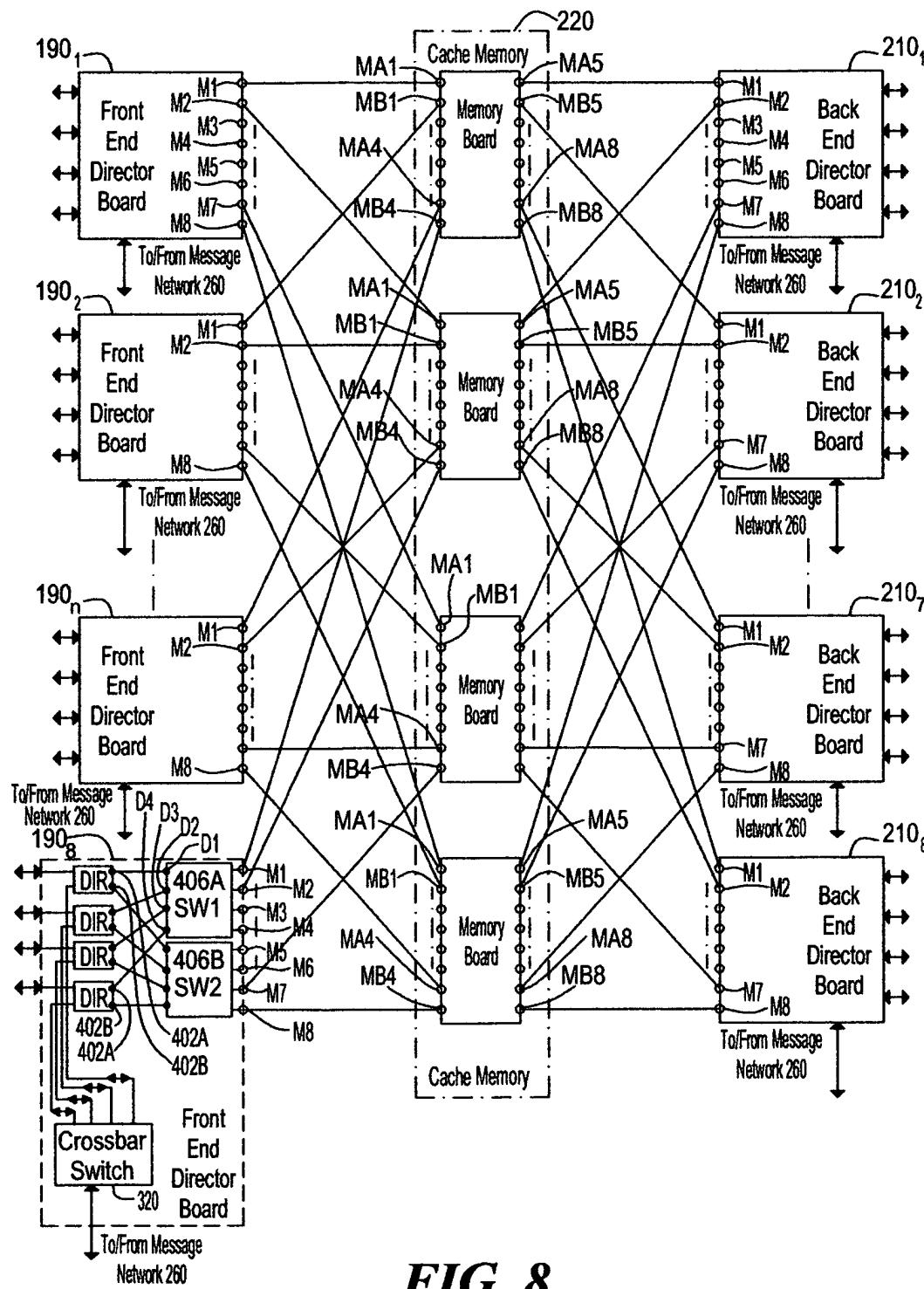


FIG. 8



Serial No.: 09/606,730

10/25

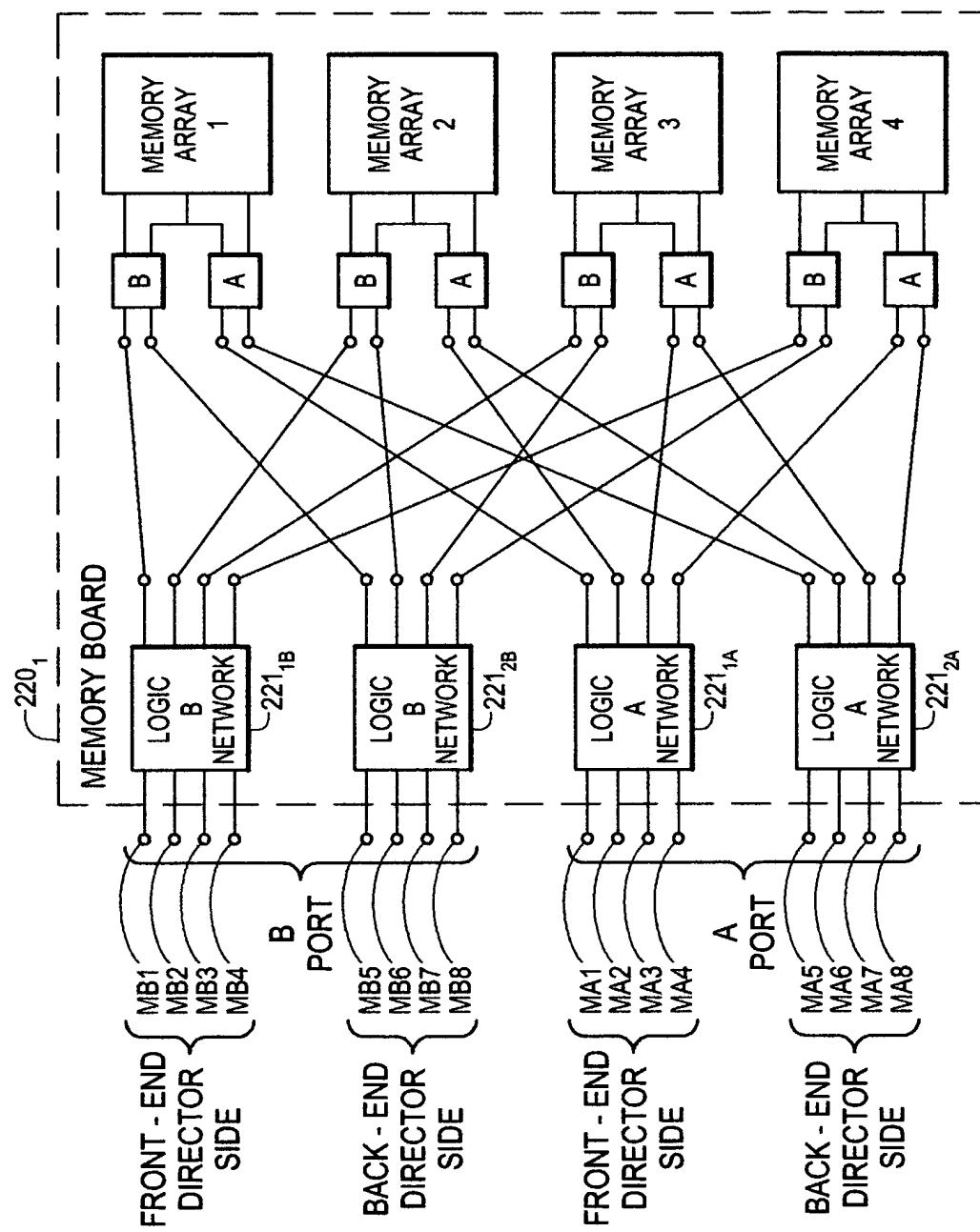


FIG. 8A



Serial No.: 09/606,730

11/25

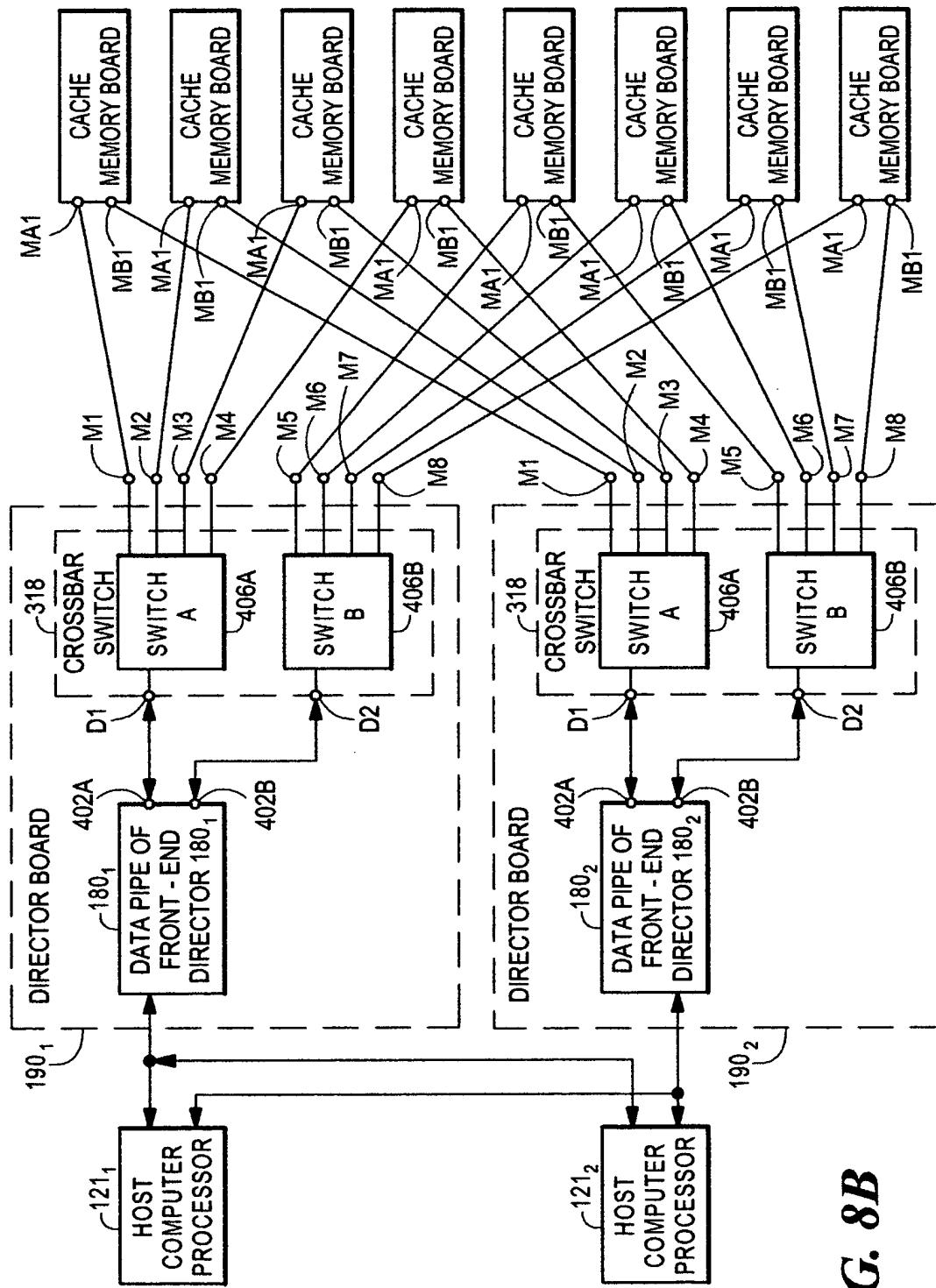


FIG. 8B



Serial No.: 09/606,730

12/25

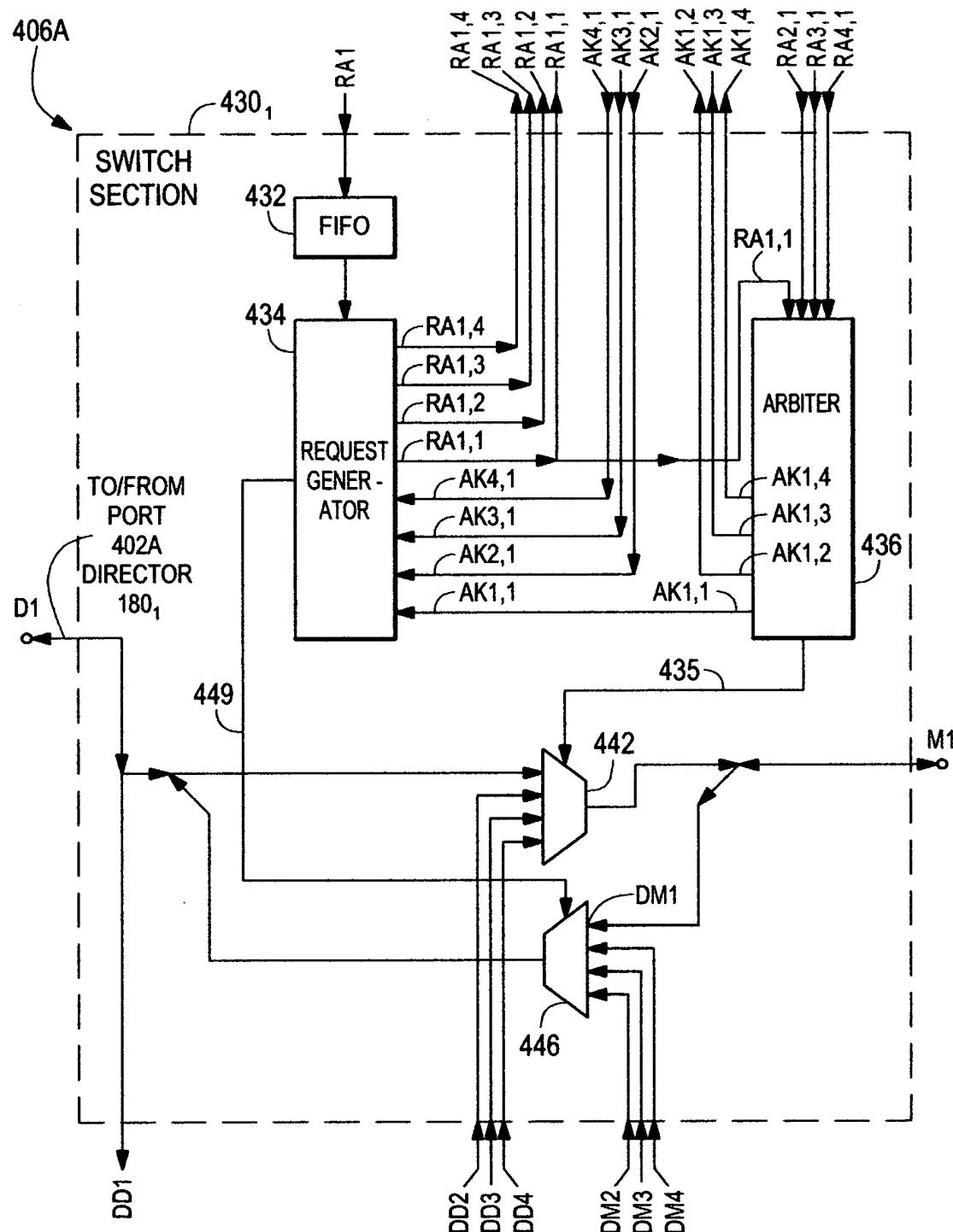


FIG. 8C

FIG. 8C-1
FIG. 8C-2

FIG. 8C-1



Serial No.:09/606,730

13/25

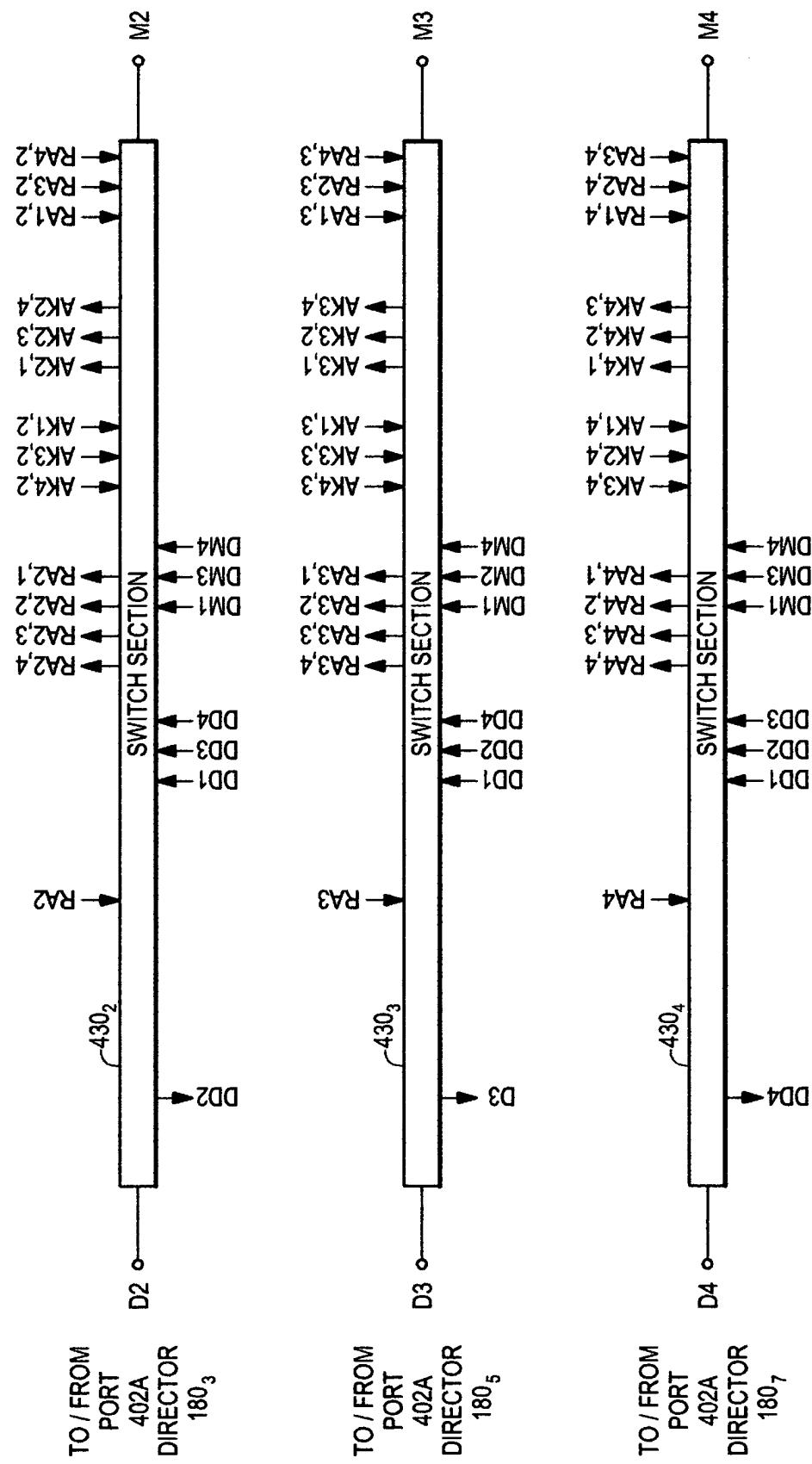


FIG. 8C-2



Serial No.: 09/606,730

14/25

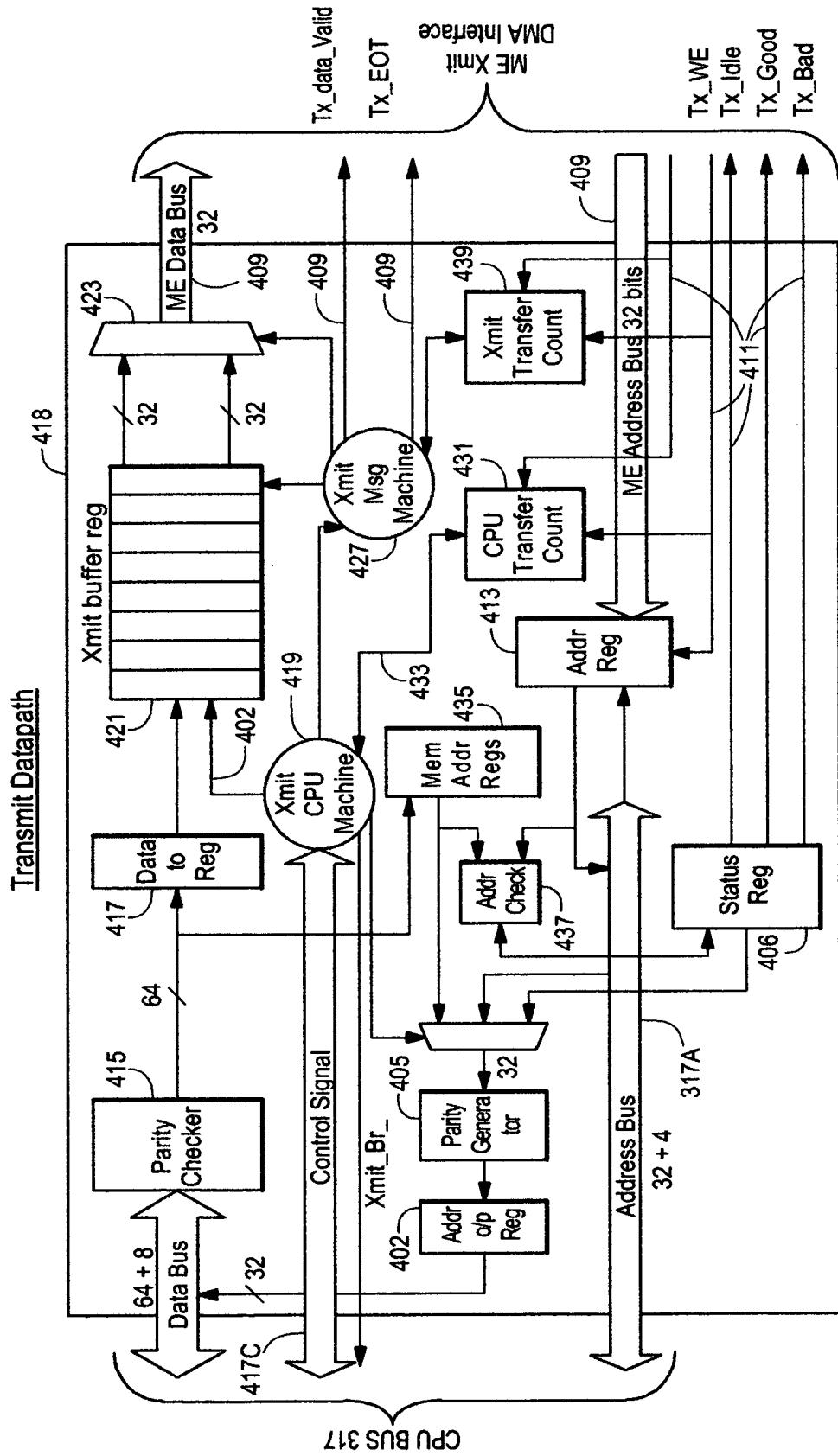


FIG. 9



Serial No.:09/606,730

15/25

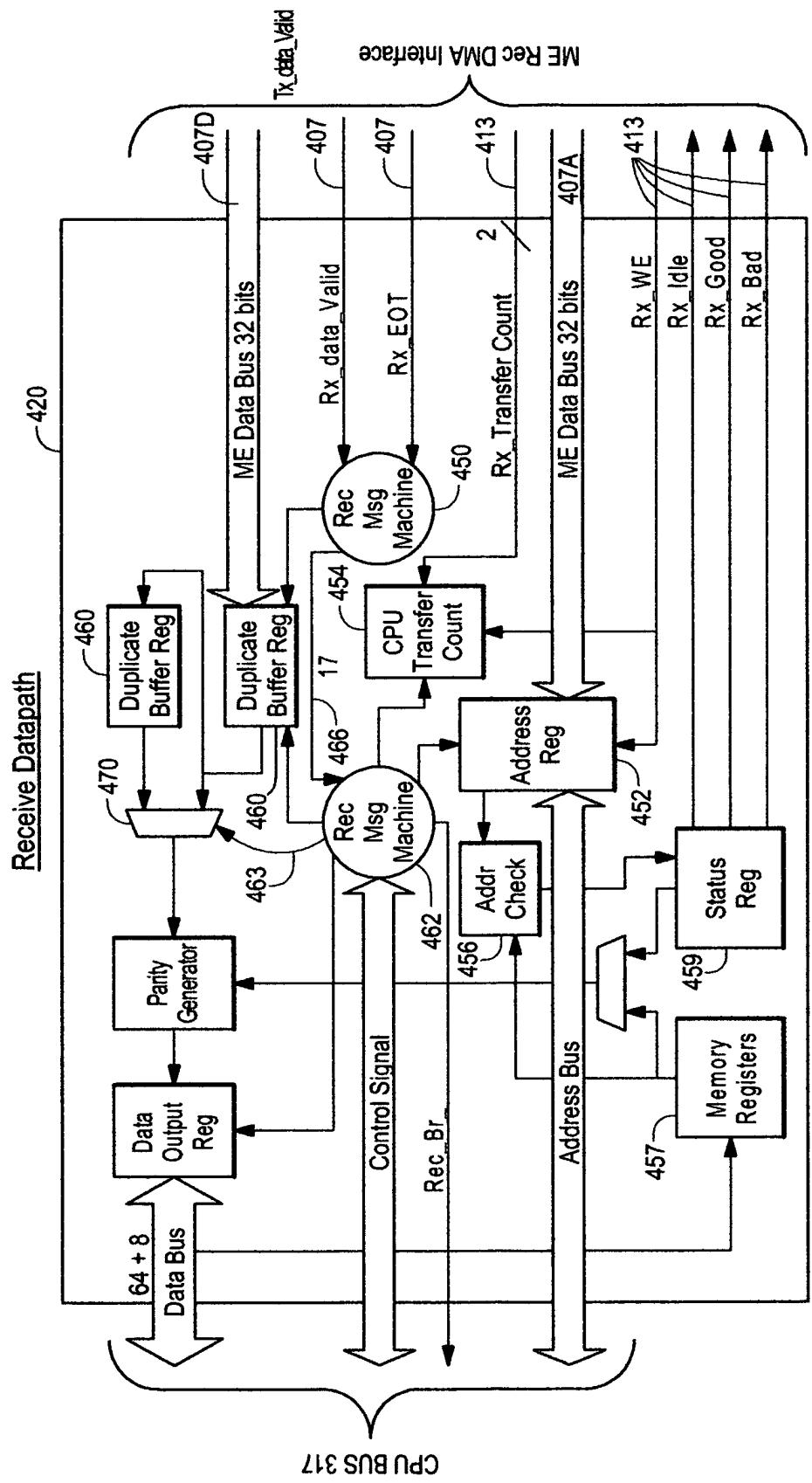
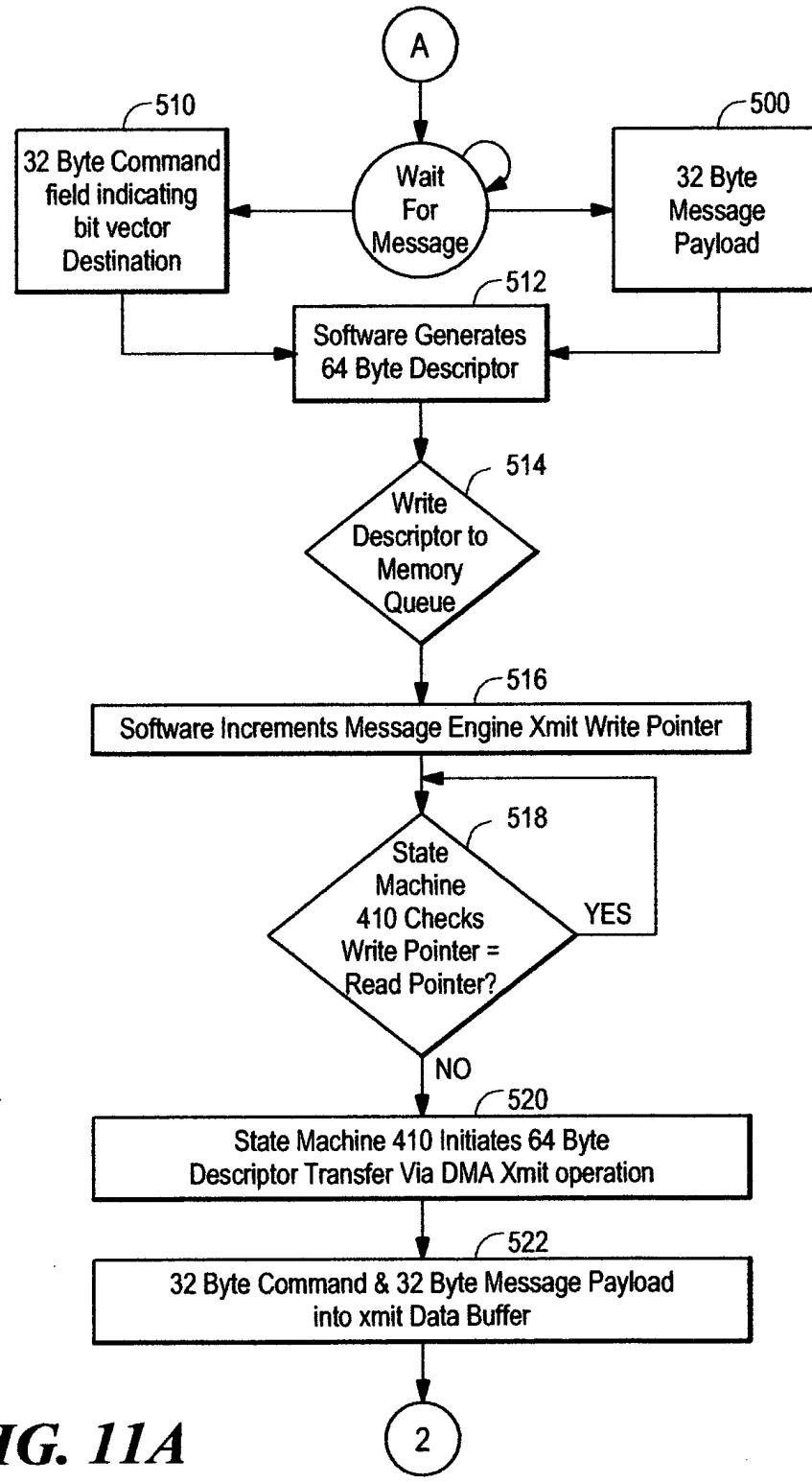
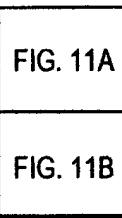


FIG. 10

**FIG. 11****FIG. 11A**



Serial No.: 09/606,730

17/25

Message Bus Send Operation Continued

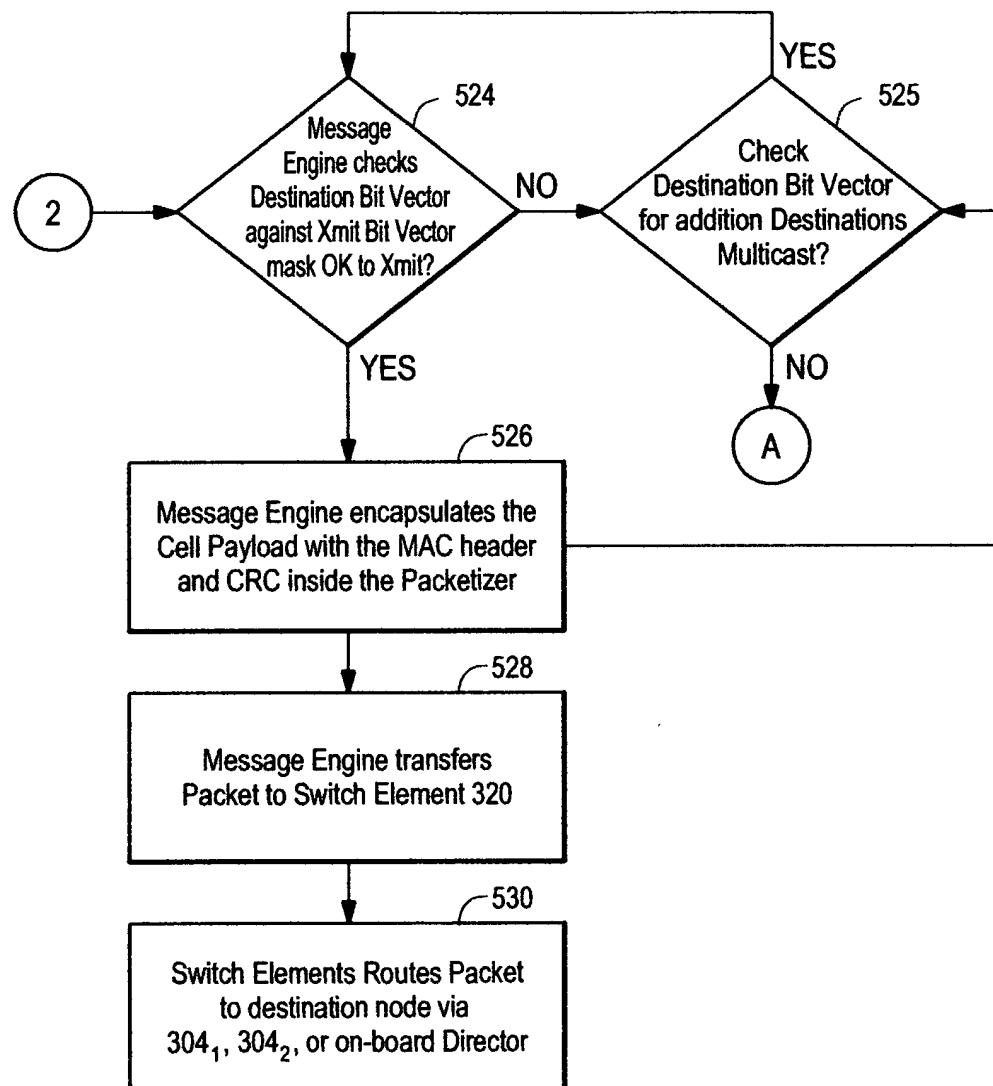


FIG. 11B



Serial No.:09/606,730

18/25

Bit Position	1	2	3	4	—	—	—	—	62	63	64
	1	0	0	0	—	—	—	—	0	0	0

FIG. 11C

Bit Position	1	2	3	4	—	—	—	—	62	63	64
	0	1	0	0	—	—	—	—	0	0	0

FIG. 11D

Bit Position	1	2	3	4	—	—	—	—	62	63	64
	0	1	1	0	—	—	—	—	0	1	1

FIG. 11E

Bit Position	1	2	3	4	—	—	—	—	62	63	64
	1	1	1	1	—	—	—	—	1	1	0

FIG. 11F

Bit Position	1	2	3	4	—	—	—	—	62	63	64
	0	1	1	0	—	—	—	—	0	1	0

FIG. 11G



Serial No.: 09/606,730

19/25

FIG. 12

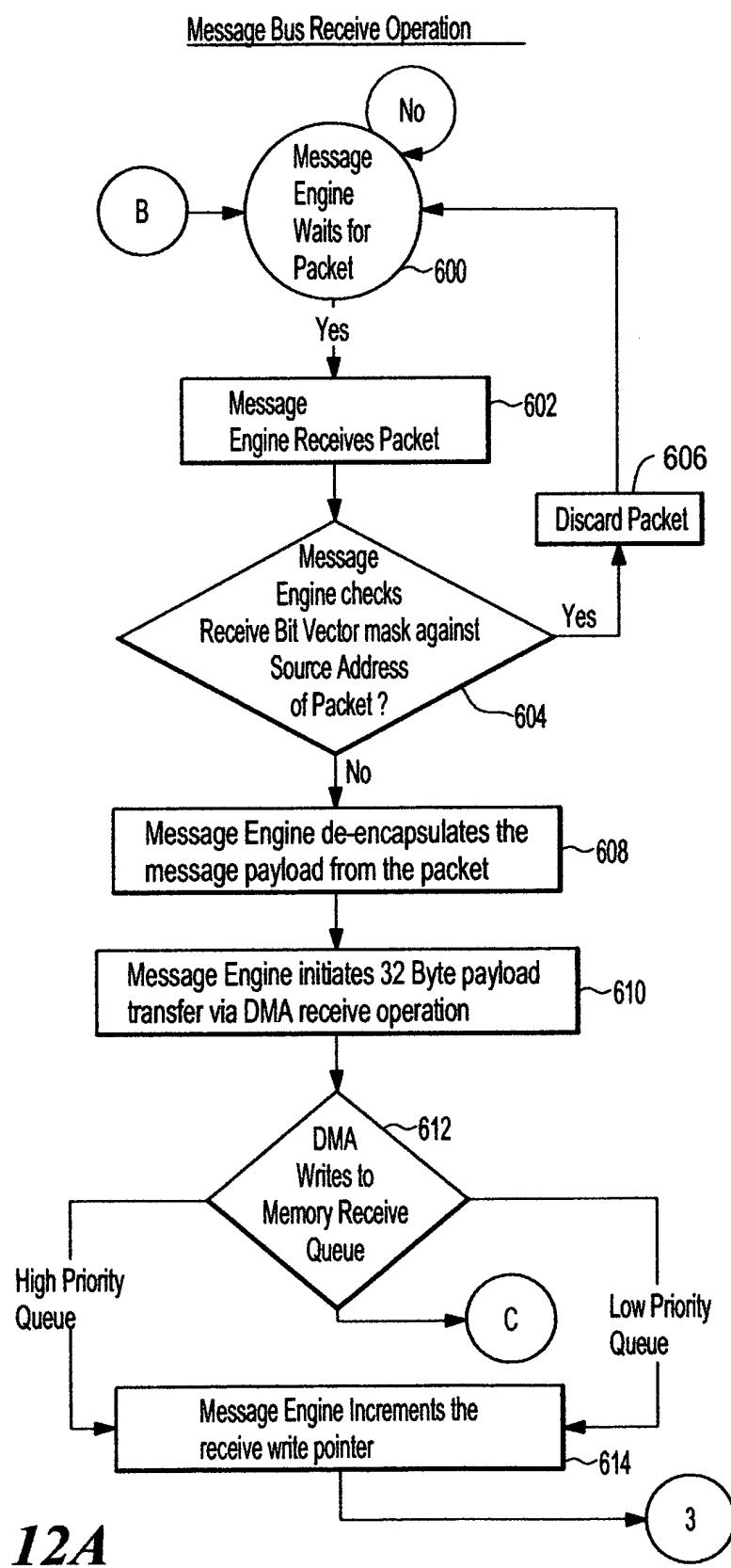
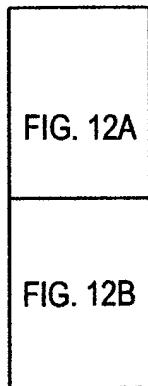


FIG. 12A



Serial No.: 09/606,730

20/25

Message Bus Receive Operation Continued

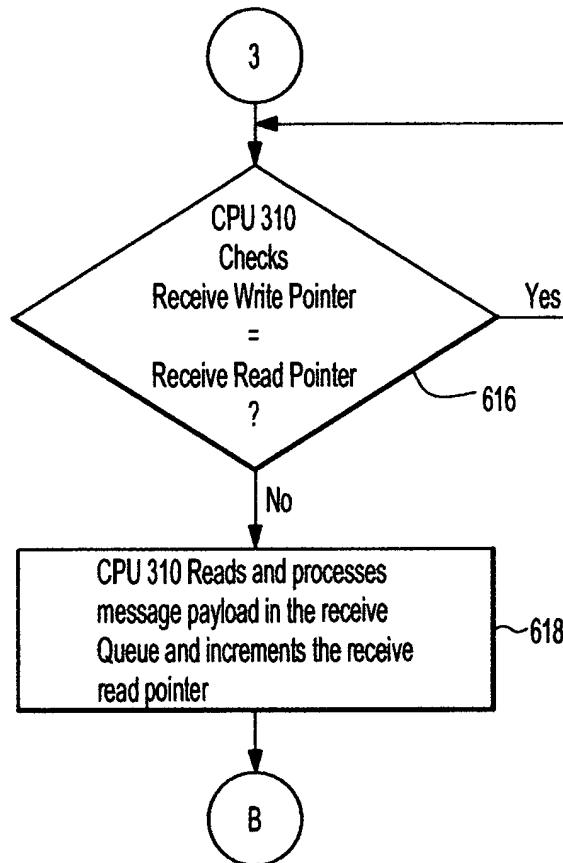


FIG. 12B



Serial No.: 09/606,730

21/25

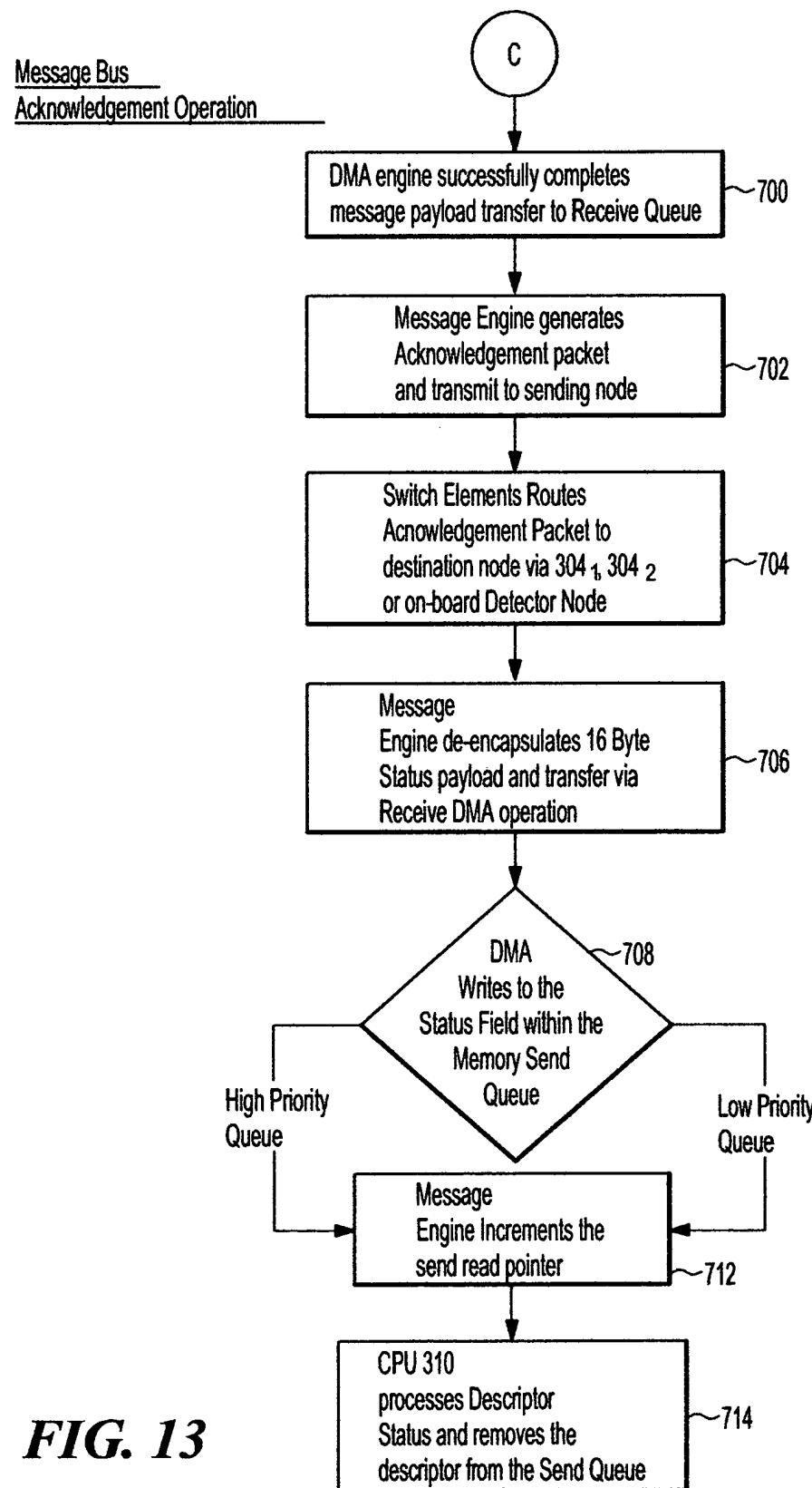


FIG. 13



Serial No.: 09/606,730

22/25

Xmit CPU flow

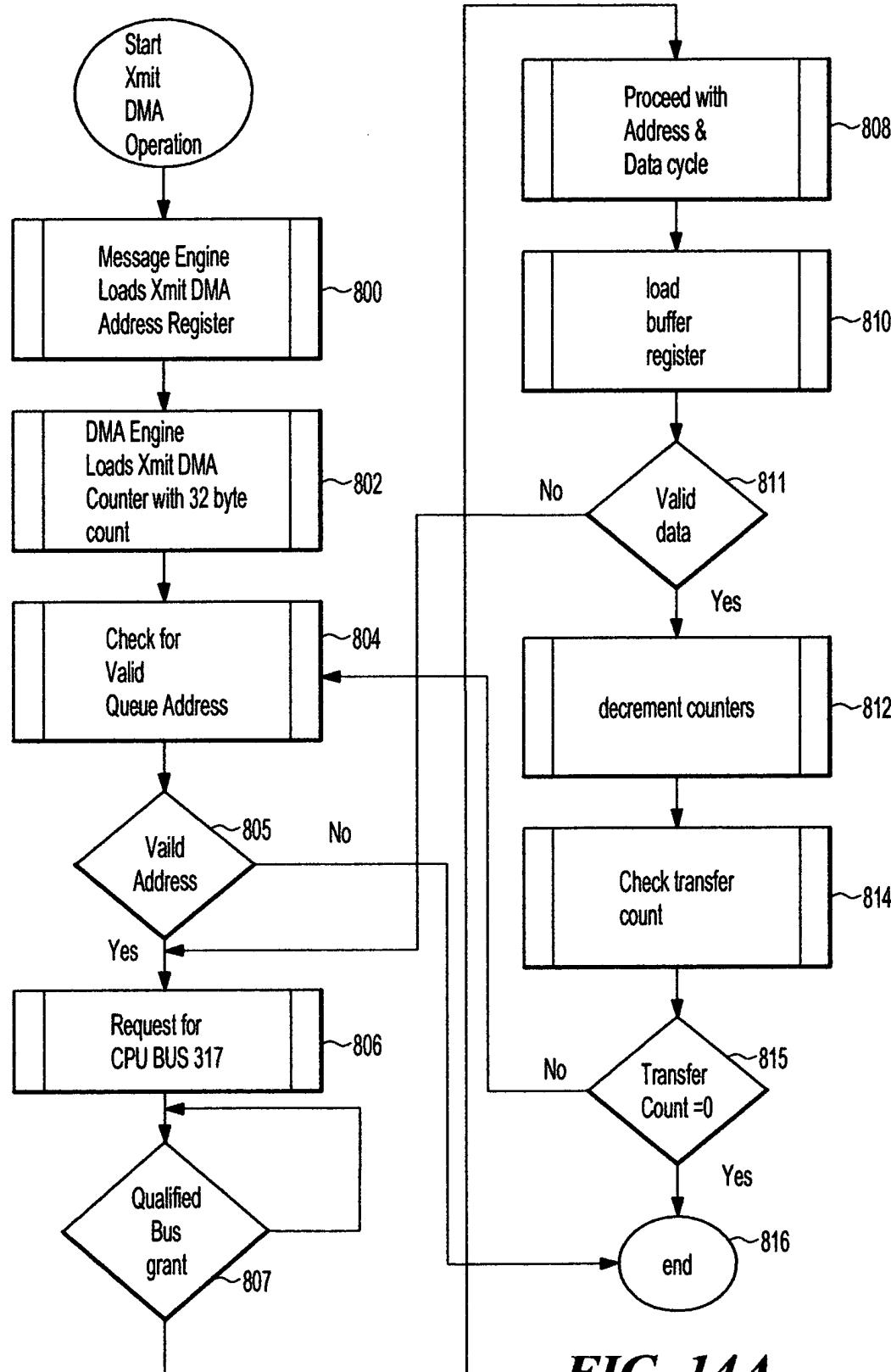


FIG. 14A



Serial No.: 09/606,730

23/25

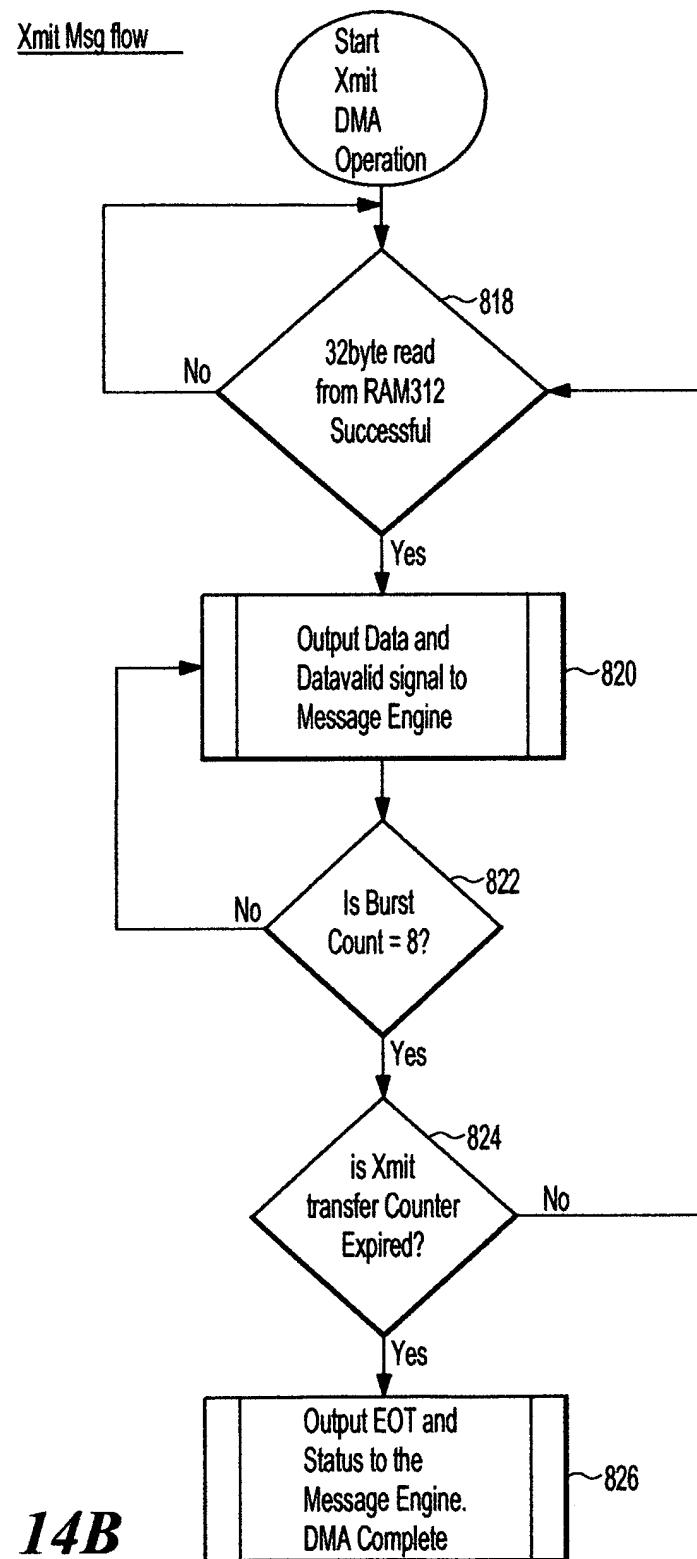


FIG. 14B



Serial No.: 09/606,730

24/25

Rec msg flow

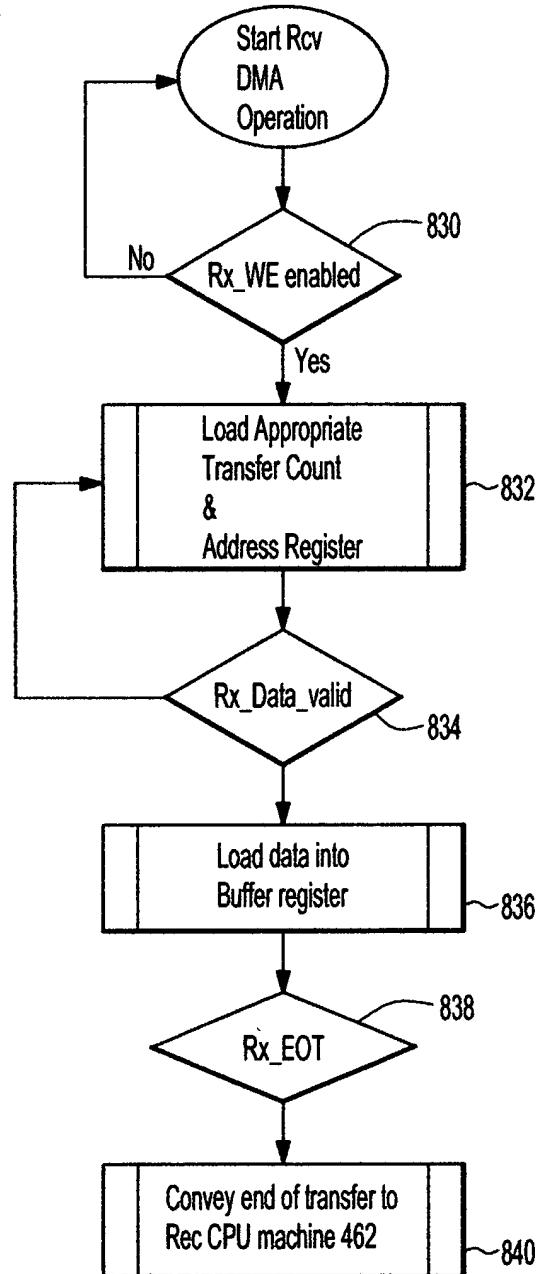


FIG. 15A



Serial No.: 09/606,730

25/25

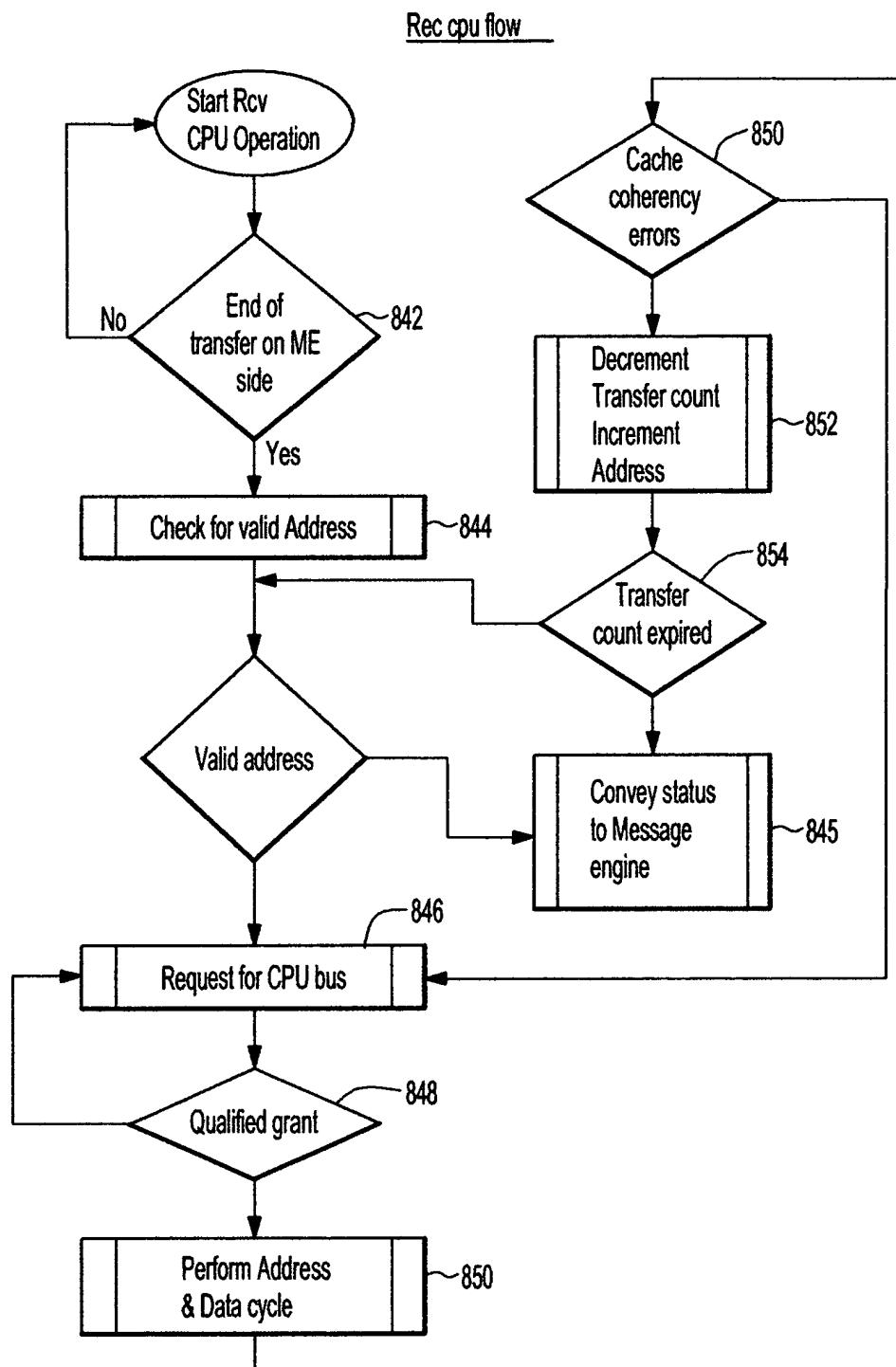


FIG. 15B